

ABSTRACT OF THE DISCLOSURE

An image processor for preventing generation of an array of dots iteratively arranged and generation of a growth pattern of dots regularly arranged in an image after binarized. The processor includes a pixel data acquisition part for acquiring image data stored in an image memory on a pixel-by-pixel basis, and a dither matrix having energy focused dots positioned in the respective cells and arranged irregularly with a non-iterative property. The processor is adapted to acquire threshold value data from the dither matrix on the basis of an address of the image data and comparing the image data of the pixel unit with the threshold value data to output a predetermined binary signal.